

<b>Notice of Allowability</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/748,963	CHEN ET AL.	
	<b>Examiner</b>	Art Unit	
	Dylan White	2819	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1.  This communication is responsive to 9/8/2007.
  2.  The allowed claim(s) is/are 8-14.
  3.  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a)  All    b)  Some\*    c)  None    of the:
    1.  Certified copies of the priority documents have been received.
    2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_ .
    3.  Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received:

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4.  A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  5.  CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
    - (a)  including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached  
1)  hereto or 2)  to Paper No./Mail Date \_\_\_\_.
    - (b)  including changes required by the attached Examiner's Amendment / Comment or in the Office action of  
Paper No./Mail Date \_\_\_\_.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
  6.  DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

- Notice of References Cited (PTO-892)
  - Notice of Draftperson's Patent Drawing Review (PTO-948)
  - Information Disclosure Statements (PTO/SB/08),  
Paper No./Mail Date \_\_\_\_\_
  - Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
  - Notice of Informal Patent Application
  - Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_.
  - Examiner's Amendment/Comment
  - Examiner's Statement of Reasons for Allowance
  - Other \_\_\_\_\_.

## DETAILED ACTION

### ***Response to Arguments***

The Examiner acknowledges the Applicants cancellation of claims 1-7.

### ***Allowable Subject Matter***

Claims 8-14 are allowed. The following is an examiner's statement of reasons for allowance:

Regarding claim 8, the upgrade method comprising: removing the second printed circuit board circuit with the nonvolatile memory from the socket on the first PCB; disposing the second PCB with the nonvolatile memory on a writer; writing a new program into the nonvolatile memory by the writer; inserting the second printed circuit board with the nonvolatile memory into the socket, wherein the new program is stored in the nonvolatile memory; and downloading the new program stored in the nonvolatile memory to the FPGA by the downloading unit.

Regarding claim 9, as being dependent on claim 8.

Regarding claim 10, the first printed circuit board having a power pin region and a plurality of I/O pin regions, where the power pin region is separated from the I/O pin regions and where each I/O terminal of the FPGA is electrically connected to a corresponding pin in the I/O pin region, all power terminals of the FPGA are electrically

connected to pins in the power pin region, and pins in the power pin region and the I/O pin region are connected to external circuits through different connectors.

Regarding claims 11-14, as being dependent on claim 10.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dylan White whose telephone number is (571) 272-1406. The examiner can normally be reached on m-f 7:30- 4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rexford Barnie can be reached on (571) 272-7492. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

DW

  
REXFORD BARNIE  
SUPERVISORY PATENT EXAMINER

09/12/07